

REMARKS

Claims 1 to 10 are retained in this application of which claims 1 to 5 have been allowed.

Claims 6 to 10 were rejected under 35 U.S.C. 103(a) as being unpatentable over Komano et al. (U.S. 5,841,054) in view of Arnold et al. (U.S. 5,908,997). The rejection is respectfully traversed.

Claim 6 requires, among other features, a general purpose processor (GPP) for reading and parsing the MIDI files stored in the flash memory to generate MIDI synthesizer commands therefrom, the DSP responsive to the MIDI synthesizer commands to synthesize audio signals and render the audio signals to the DAC via the at least one DSP peripheral device to implement a MIDI synthesizer. No such feature is taught or even remotely suggested by Komano et al., Arnold et al. or any proper combination of these references either alone or in the total combination as claimed. Note that the DSP of Komano et al. drives the DAC directly.

Claim 7 requires, among other features, second data processing means for reading and parsing the MIDI files stored in the data storing means to generate MIDI synthesizer commands therefrom, the first data processing means responsive to the MIDI synthesizer commands to synthesize audio signals and render the audio signals to the data converting means via the driving means to implement a MIDI synthesizer. No such feature is taught or even remotely suggested by Komano et al., Arnold et al. or any proper combination of these references either alone or in the total combination as claimed. Note that the DSP of Komano et al. drives the DAC directly.

Claims 8 to 10 depend from claim 7 and therefore define patentably over the applied references for at least the reasons presented above with reference to claim 7.

In view of the above remarks, favorable reconsideration and allowance are respectfully requested.

Respectfully submitted,



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